

MEMORY SYSTEM MOUNTED DIRECTLY ON BOARD AND ASSOCIATED METHOD

ABSTRACT

5 The invention provides an improved memory system that addresses signal degradation due to transmission line effects. The improved memory system includes a first buffer, at least one first memory device coupled to the first buffer, and a plurality of signal traces. The first buffer and memory device are mounted on a motherboard. Likewise, the plurality of signal traces is routed on the motherboard. Doing so eliminates stub loads that cause signal
10 reflection that, in turn, result in signal degradation.